AMENDMENTS TO THE SPECIFICATION

IN THE SPECIFICATION:

Please replace the second paragraph beginning on Page 9, line 12, with the following amended paragraph:

In order to shorten the time required for the state change to occur, it is possible to use a binary counter instead of the octal counter so that the state change occurs when two self-running synchronizing pulses or two horizontal sync signals blocked by the gate have been counted. However, in that case, if three false synchroniziong pulses are input in succession, the third false synchronizing pulse is output as a horizontal sync signal, as shown in Fig. 16.